

Interfacial phase-change memory

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Phase-change memory technology relies on the electrical and optical properties of certain materials changing substantially when the atomic structure of the material is altered by heating¹ or some other excitation process^{2–5}. For example, switching the composite Ge₂Sb₂Te₅ (GST) alloy from its covalently bonded amorphous phase to its resonantly bonded metastable cubic crystalline phase decreases the resistivity by three orders of magnitude⁶, and also increases reflectivity across the visible spectrum^{7,8}. Moreover, phase-change memory based on GST is scalable^{9–11}, and is therefore a candidate to replace Flash memory for non-volatile data storage applications. The energy needed to switch between the two phases depends on the intrinsic properties of the phase-change material and the device architecture; this energy is usually supplied by laser or electrical pulses^{1,6}. The switching energy for GST can be reduced by limiting the movement of the atoms to a single dimension, thus substantially reducing the entropic losses associated with the phase-change process^{12,13}. In particular, aligning the *c*-axis of a hexagonal Sb₂Te₃ layer and the <111> direction of a cubic GeTe layer in a superlattice structure creates a material in which Ge atoms can switch between octahedral sites and lower-coordination sites at the interface of the superlattice layers. Here we demonstrate GeTe/Sb₂Te₃ interfacial phase-change memory (IPCM) data storage devices with reduced switching energies, improved write-erase cycle lifetimes and faster switching speeds.

A physical vapour deposition system was used to grow GeTe–Sb₂Te₃ superlattices with GeTe and Sb₂Te₃ layer thicknesses between 5 Å and 40 Å (herein named interfacial phase-change materials, IPCM). Figure 1a presents a typical example of an IPCM structure grown on an oxidized silicon wafer. The local atomic structure transformed into the cubic crystalline phase at a temperature of 155 ± 10 °C, which is consistent with the crystallization temperature of the Ge₂Sb₂Te₅ (GST) composite alloy^{6,10,14}, but lower than the crystallization temperature of GeTe confined within thicker multilayer structures (190–250 °C)¹⁵.

In the somewhat random local atomic structure of amorphous GST, Ge atoms can occupy both threefold and tetrahedral sites^{16,17} (see Supplementary Fig. S1) with principally covalent bonding; this bonding phase is technologically known as the RESET state. In contrast, the cubic crystalline phase is assembled from approximately octahedral sub-units, which are usually described as being ‘resonantly’ bonded^{7,8,18}; this bonding phase is technologically known as the SET state. Given the absence of significant long-range order in nanoscale structures, it is appropriate to discuss the different phases of the IPCM in terms of the bonding nature rather than as being amorphous or crystalline. Herein, the covalent (RESET) and resonant (SET) bonding states will be used when referring to these two phases.

To determine the maximum rate at which data can be written to phase-change random access memory (PCRAM) devices, it is

necessary to determine the switching time between the covalently and resonantly bonded phases. The switching time for GST and IPCM samples was measured optically using a laser pump–probe static tester system (see Methods). Figure 1b shows the change in the intensity of the probe light through laser RESET areas in the IPCM film as a function of time during and after the application of a 100 ns pump pulse. Four different regions are discernible: (i) no change in transmission, (ii) a reduction in transmission (indicative of a RESET to SET transition), (iii) an increase in transmission and then, after 100 ns, subsequent crystallization (indicative of a melt-SET transition) and (iv) a two-step transmission increase, which indicates melting and subsequent partial ablation. The plot shows that, for higher laser pump powers, the IPCM could transform into the SET phase in just 25 ns, but continued heating by the 100 ns pulse caused melting and subsequent ablation. In contrast, complete crystallization into the SET phase of the GST film was only observed when the pulse length was increased to at least 80 ns under otherwise identical conditions. Figure 1c also shows that, for low-power laser pulses (9.5 mW in this case), the transition rate of the IPCM to the SET phase is approximately four times greater than that of the GST film. Higher-power (16.5 mW) and shorter pulses with a duration of 25 ns significantly shortened the period of time before crystal growth commenced and allowed complete crystallization without subsequent damage. It should be mentioned that similar enhancements in switching performance were observed in IPCM digital versatile disc measurements (Supplementary Fig. S2).

The transition rate to the resonant (SET) phase is dependent on the structure of the covalent (RESET) phase, which is influenced by the preparation conditions^{19,20}. Density function theory (DFT) modelling has shown that quenching GST from a molten state produces a covalent phase where the Ge atoms occupy a mixture of octahedral and lower coordination sites^{21,22}. Crystallization then proceeds from the pre-existing octahedral configurations embedded within the covalent phase^{21,23}. This is most evident in the crystallization time of sputter-deposited, covalent GST films, which is significantly longer than that of laser melt-quenched^{11,24} GST films. Essentially, as-deposited GST films require additional time to form the octahedral structural sub-units, which seed subsequent crystallization^{21,23} into the cubic phase. This implies the existence of an optimum covalent GST structure that incorporates sufficient octahedral seeds for efficient crystallization.

The covalent IPCM nanostructures consist of atomic planes of covalently bonded Ge atoms separated by planes of crystalline *R*3*m* Sb₂Te₃. Applying simplified models for the idealized phase-transition process^{8,12}, one can speculate that all Ge atoms are located at the Sb₂Te₃ interface. During the phase transition, this interface allows the Ge atoms to switch into the resonantly bonded octahedral sites and it can, therefore, be considered a plane of octahedral nucleation centres, which provides a template²³

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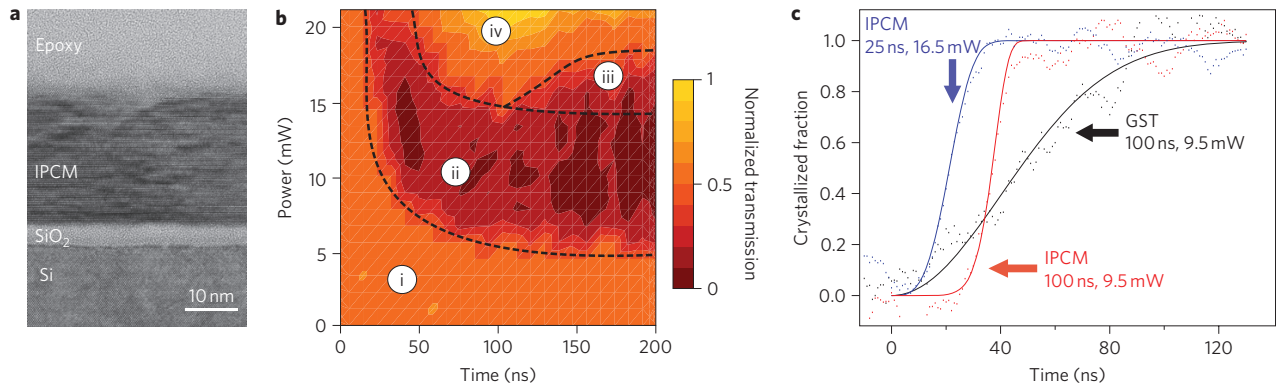


Figure 1 | Optical pump-probe testing of IPCM switching behaviour. **a**, High-resolution transmission electron micrograph TEM image of a typical as-grown $(\text{GeTe})_2(\text{Sb}_2\text{Te}_3)_4$ interfacial phase-change material on silicon. The $(\text{GeTe})_2$ layers are 1 nm thick, and the $(\text{Sb}_2\text{Te}_3)_4$ layers are 4 nm thick. **b**, Time-resolved pump-probe static tester measurement for the RESET to SET transition process of a 400-nm-radius laser RESET mark in the IPCM film. The RESET mark was created with a laser pulse with a duration of 40 ns and a power of 32 mW. The normalized optical transmission of a 100 μW probe beam through the RESET mark is plotted as a function of time during and after the 100 ns laser pump pulse for varying incident optical powers. Four regions can be discerned: (i) no change in transmission, (ii) a reduction in transmission (indicative of a transition from the RESET state to the SET state), (iii) a slight increase followed by a reduction in transmission (indicative of melting then subsequent crystallization into the SET state), and (iv) an increase in transmission (indicative of melting then partial ablation). **c**, Re-crystallized fraction of the 400-nm-radius RESET mark (created with a 40 ns, 32 mW laser pre-pulse) as a function of time for GST (100 ns, 9.5 mW pump pulses; black); IPCM (100 ns, 9.5 mW pump pulses; red); IPCM (25 ns, 16.5 mW pump pulses; blue), respectively.

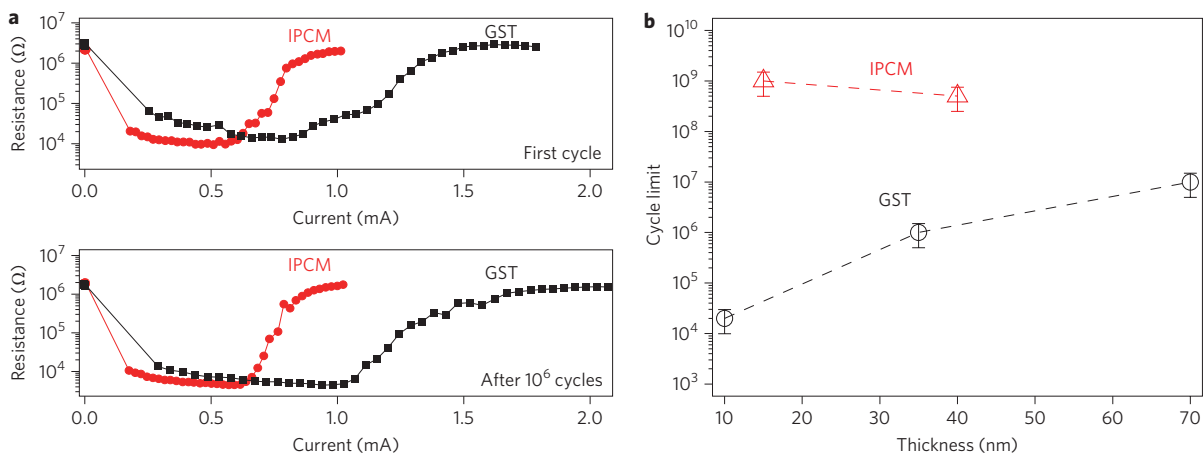


Figure 2 | Electrical switching characteristics of IPCM devices. **a**, Plots of resistance versus current for PCRAM devices in the first cycle (upper panel) and after 1×10^6 cycles (lower panel). Filled squares are from a device fabricated from a single GST target, and filled circles are for a device containing a $(\text{GeTe})_4(\text{Sb}_2\text{Te}_3)_2$ IPCM. The SET pulse lengths were 50 ns and 100 ns for the IPCM and GST materials, respectively. The RESET pulse length was fixed at 50 ns for both the IPCM- and GST- based devices. **b**, Maximum number of SET-RESET cycles plotted as a function of phase-change material thickness. The cyclability of phase-change memory cells based on the GST material shows a strong dependence on film thickness (black circles), whereas the cyclability of the IPCM (red triangles) based on repeated blocks of $(\text{GeTe})_2(\text{Sb}_2\text{Te}_3)_2$ shows little sensitivity to total film thickness. Dashed lines have been included to guide the eye.

for enhanced cubic crystal growth. It should be noted that the covalent and resonantly bonded IPCM structures are stable when relaxed according to DFT calculations.

The homogeneous nature of the IPCM means that each Ge atom experiences the same force, so, for the ideal interface, the distribution of transition energies required by the Ge atoms is a delta function. This allows a fast, coherent switching motion into the octahedral sites when the threshold energy is acquired from either laser irradiation or Joule heating, hence the abrupt IPCM crystallization process observed in Fig. 1c. In contrast, re-amorphized GST is less ordered, so the Ge atoms experience different forces, resulting in a broad distribution of transition energies. A long crystallization pulse (>80 ns) was therefore necessary to completely switch all of the Ge atoms. The point at which crystal growth begins is dependent on the covalent structure and laser power. For the IPCM covalent structure, the Sb_2Te_3 layers

provide pre-existing nucleation interfaces that facilitate crystal growth without the time-consuming crystal nucleation process, so complete crystallization is possible in just 25 ns. It was not possible to crystallize amorphous marks in the GST film with such short pulse lengths.

So far, pump-probe techniques have allowed us to corroborate the superior switching and energy performance of GeTe-Sb₂Te₃ nanostructured IPCMs. However, to see the true potential of this enhanced material it is necessary to demonstrate its utilization in real data storage devices. Here, the discussion turns to electrical solid-state memory cells where low-energy, high-speed operation is essential for environmentally friendly data storage.

In Fig. 2a the resistance R of IPCM- and GST-based PCRAM cells is plotted against the applied electric current I . The plot clearly shows that the currents necessary to reversibly switch IPCM-based devices between the SET and RESET states are

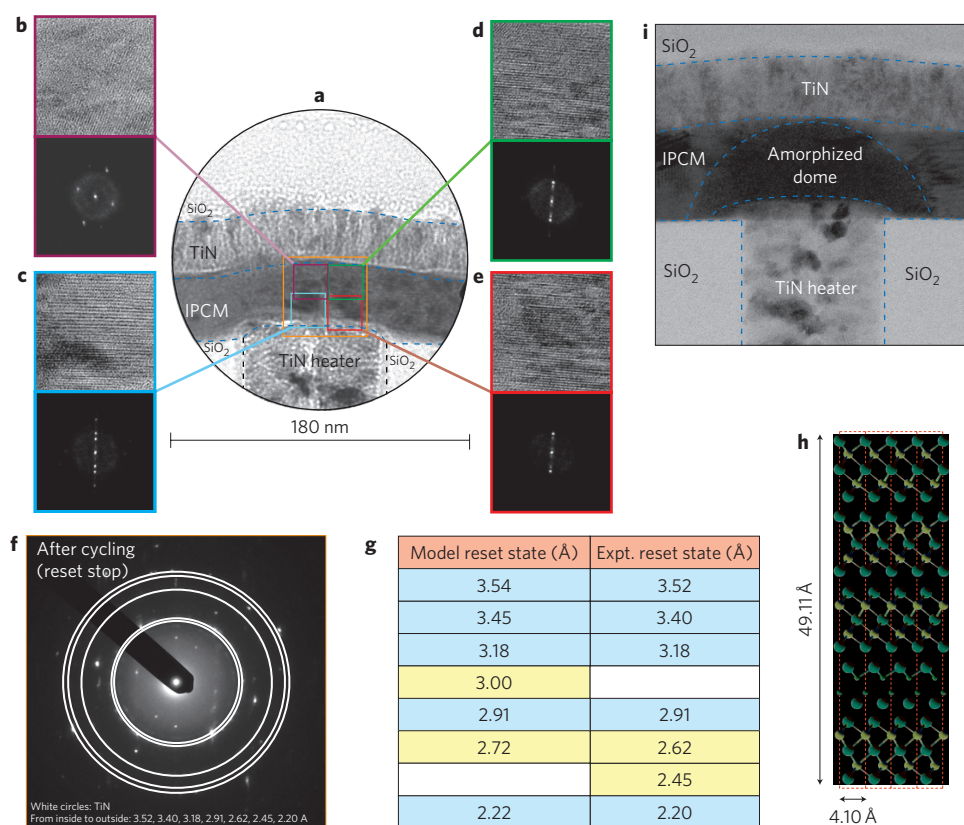


Figure 3 | Analysis of the RESET state. **a**, TEM images of a $(\text{GeTe})_2(\text{Sb}_2\text{Te}_3)_4$ IPCM structure in the RESET state after 1×10^3 SET-RESET cycles. In contrast to GST, the TEM image shows that there is no amorphous region surrounding the TiN heating electrode. **b–e**, High-resolution TEM images (top) and SAD patterns (bottom) for the four regions inside the coloured squares in **a**. The layered IPCM structure and associated superlattice diffraction spots are clearly visible in all images. **f**, Selective area electron diffraction pattern of the whole IPCM structure. The white concentric rings originate from the TiN electrodes. **g**, DFT calculations of the inter-planar distances in $(\text{GeTe})_2(\text{Sb}_2\text{Te}_3)_4$ (left column) are in good agreement with the distances determined from the diffraction pattern. **h**, Model used in DFT calculations: Ge, Sb and Te atoms are coloured green, purple and orange, respectively. **i**, An IPCM device that was deliberately RESET with the same high-power pulse conditions required by GST. As with GST, a melt-amorphized dome is formed above the TiN heater, resulting in destruction of the superlattice structure and irreversible damage to the IPCM device.

substantially lower than those required for identical devices based on GST. Indeed, the electrical energy used to SET the GST and IPCM devices was 90 pJ and 11 pJ, respectively. Furthermore, due to the homogeneity of the Ge switching environment, the switch (or ‘current snap’) between the SET and RESET states was substantially more abrupt for the IPCM cells. This produced a tighter distribution of device characteristics, thus facilitating accurate determination of the SET and RESET states.

It is tempting to explain these improvements in device characteristics as a result of increased thermal confinement due to the layered nature of the IPCM structure²⁵. The thermal conductivity of both IPCM and GST films was directly measured by a standard pump-probe, thermoreflectivity technique²⁶, the most suitable technique available for measuring thermal conductivity perpendicular to the surface of thin-film materials. In contrast with thicker multilayered phase-change films²⁷, heat was found to diffuse through the IPCM faster than that for an otherwise identical film deposited from a composite target (Supplementary Fig. S3). This implies that the thermal boundary resistance between the individual Sb_2Te_3 and GeTe layers is negligible²⁵. The actual thermal conductivities were calculated to be $0.33 \text{ W m}^{-1} \text{ K}^{-1}$ for the IPCM and $0.21 \text{ W m}^{-1} \text{ K}^{-1}$ for the GST film. These measurements explicitly show that the enhanced phase-change performance of IPCM cannot be explained by reduced thermal conductivity.

Entropy arguments, however, can explain the efficiency improvements observed for IPCM. The crystallization process in GST is

three-dimensional; that is, atomic movements occur stochastically in all directions²¹. The covalent state is composed of a range of local atomic configurations¹⁶, so it has a relatively large entropy. In contrast, the IPCM structures, reported here, were designed to minimize the change in configurational entropy between SET and RESET states. The ultrathin, uniform stratum of Sb_2Te_3 and GeTe lowers the entropy of the covalent state by restricting the number of atomic configurations that can exist. For the SET operation, IPCM-based devices use just 12% of the energy required by similar GST-based devices, an improvement consistent with the theoretical efficiency enhancement¹³. This increase in efficiency means that less energy is wasted during the phase transition, which in turn leads to more than an order of magnitude improvement in the SET-RESET cyclability¹¹. The enhanced cyclability is most clear in Fig. 2b, which shows the number of times the GST and IPCM PCRAM cells could be cycled before failure for films of different thicknesses. The IPCM cycling performance shows little dependence on film thickness, but the cycling performance of the GST cell diminishes as the film thickness is reduced. These significant increases in device performance were only possible for Sb_2Te_3 -GeTe interfaces with crystallographic texture normal to the substrate; poor-quality interfaces with multiple grains required substantially higher SET \leftrightarrow RESET switching energies and resulted in fewer cycles being completed before failure.

Reducing the entropic losses has the further ramification of producing device characteristics that are highly repeatable. Indeed, the

resistance of the IPCM during the first SET–RESET cycle is identical to that of the millionth cycle (see Fig. 2a). This implies that the IPCM structure is sustained for at least one million write–erase cycles. In contrast, as-deposited GST cells require more than 100 cycles before the structures of the SET and RESET states settle to give reproducible resistances; this is typical for GST^{28,29}. The structure of the IPCM device in the RESET state after extended cycling is shown in Fig. 3a. The amorphous dome-shaped region is absent and, more importantly, a closer inspection of the IPCM directly above the heating electrode (Fig. 3b–e), reveals that the structure remains ordered in layers. Selective area diffraction (SAD) patterns associated with these images similarly display superlattice reflections, thus proving that, even after cycling, the layer structure is still present. In Fig. 3b, the direction of the layers is at a slight angle with respect to the growth plane. This tends to occur for thicker films above the corner of the heater plug and can be avoided for thinner IPCM films. The SAD pattern collected from the whole IPCM structure (Fig. 3f) confirms the overall crystallinity of the cell. Interplanar distances calculated from the diffraction spots show excellent agreement with DFT models of Ge atoms in a low-coordination state at the Sb₂Te₃–GeTe interface (Fig. 3g,h), indicating a non-melting transition of the Ge atoms. In contrast, the RESET state of GST-based PCRAM cells exhibits a dome-shaped amorphous region above the heating electrode that results from melt-quenching during the RESET process¹¹. For the purpose of comparison, Fig. 3i shows an IPCM cell that was deliberately RESET with the same high-power electrical pulse conditions required by GST (1.25 mA at 6 V for 50 ns). As with GST, a dome-shaped melt-amorphized region formed above the TiN heating electrode. Resetting with such large electrical currents destroyed the IPCM superlattice structure and the device therefore required much larger SET and RESET currents for further switching operations.

For GST it is generally accepted that atomic diffusion towards the memory cell's electrodes is a major cause of device failure^{30,31}. Because the atoms within a thin phase-change film need only diffuse over a relatively short distance in order to meet the cell's electrodes, one would expect thinner phase-change films to fail with fewer cycles than thicker films; this dependency is demonstrated for the GST samples shown in Fig. 2b. The IPCM cells, however, do not show any strong dependence on film thickness, and even films 15 nm thick can be switched between the SET and RESET states more than one billion times, a characteristic that will become increasingly important as memory cells are further reduced in size to meet the demands of higher data storage densities. In contrast, GST devices of a similar thickness fail within 20,000 SET–RESET cycles. These measurements, in combination with the negligible changes in electrical resistance during cycling, the lack of an amorphous dome at the heating electrode and the cycle durability of the layered structure provide substantial evidence that interlayer diffusion is negligible in the IPCM structure. These results are consistent with recent *in situ* measurements of excitation-assisted amorphization in GST³ and other chalcogenides^{32–34}, switching models based on resonant bonding arguments^{5,8,18} and ultrafast, low-energy optical manipulation of atomic arrangements in IPCM structures⁴, all of which occur without melting in the conventional sense. In contrast to GST memory cells, which due to repeated switching-induced melting tend to show migration of Ge and Sb atoms towards the cathode and Te towards the anode³⁰, composition analysis of the cycled IPCM cell did not show migration of the atomic species (see the energy-dispersive X-ray spectroscopy data in the Supplementary Information). This observation is consistent with a non-melting IPCM RESET operation as well as reported measurements of substantially reduced atomic diffusivities in the electrically stressed solid films of GST³¹.

Despite the apparent similarities between the IPCM and other layered (superlattice-like) phase-change structures^{15,27}, the difference

in the materials' operation is clear when one compares the performance within a device. The IPCM devices used for the electrical switching measurements shown in Fig. 2a consisted of GeTe layers less than 2 nm thick, so all Ge atoms were close to an Sb₂Te₃ interface. Consequently, a single SET event is observed for the IPCM, and the Sb₂Te₃ layers act as inert crystalline seeding layers. In contrast, the thicker layers within the superlattice-like structures show independent crystallization events for the GeTe and Sb₂Te₃ layers¹⁵. The IPCM-based devices also show two orders of magnitude change in resistance (same as for GST-based devices), whereas the superlattice-like structures show one order of magnitude change¹⁵. Furthermore, the change in configurational entropy has been minimized for the IPCM, so its SET current is an order of magnitude lower than that reported for the superlattice-like structures^{15,27}, a result that is in good agreement with calculated efficiency enhancements¹³.

The SET process for the PCRAM devices and static tester measurements show that significant efficiency enhancements are possible by implementing an IPCM structure. In particular, compared with GST, the IPCM nanostructure uses an order of magnitude less energy for the SET operation. These results, in conjunction with the fact that the IPCM structure can withstand repeated cycling, indicate that the enhanced IPCM device performance is correlated with a reduced entropy difference between the SET and RESET states. Switching between these states is possible without melting the IPCM structure, so the probability of long-range atomic diffusion and associated damage to the device is reduced. The lower energy consumption of the IPCM switching process results in a correlated¹¹ order of magnitude increase in the SET–RESET cycle endurance ($>1 \times 10^7$ cycles) when compared with GST ($<2 \times 10^6$ cycles). We have also found that thicker Sb₂Te₃ layers do not significantly influence the rate of crystallization but can reduce the stress during the switching process, thus allowing switching at even lower energies and further increases in the number of SET–RESET cycles. Supplementary Table 1 summarizes the main measurements performed on both GST and IPCM materials.

In conclusion, the interface between the GeTe and Sb₂Te₃ controls the local atomic switching of Ge atoms resulting in a phase transition with substantially reduced entropic losses. As a result, the IPCM RAM devices consume an order of magnitude less energy during the SET process and show enhanced switching responsiveness with respect to their GST counterpart. On the basis of these results we believe the most efficient and fastest phase-change memory devices will be developed using nano-structured materials that take advantage of entropy controlled switching and, furthermore, exhibit effective electrical properties that are unattainable in GST. This work has introduced further degrees of freedom to tailor the performance of the data storage devices on which the modern world has become reliant.

Methods

For the static tester measurement, a dual laser system was used to measure the switching time of the IPCM and GST materials. The transmitted intensity of the probe laser beam (633 nm) was measured while the same spot on the film was excited by a pulsed 650 nm pump laser. Owing to the substantial differences in the transmittance of the films between the resonant and covalent bonding states, the transmitted intensity of the probe was assumed to decrease in proportion to the fraction of the resonantly bonded phase, and a relative comparison of the transition rate is possible by fitting the data using a Johnson–Mehl–Avrami–Kolmogorov-like equation³⁵, which describes the kinetics of solid-state phase transformations. For the 100 ns, 9.5 mW pump pulse, the rate of crystal growth from the RESET state for the IPCM was more than four times greater than for the GST film irradiated with an identical pulse (Fig. 1c). Further details are given in the Supplementary Information.

PCRAM cells based on a 75 nm TiN heating electrode (Fig. 3a) containing both standard GST and IPCM films were compared in terms of device performance. Figure 2a shows that the cells were initially in the high-resistance covalent state. PCRAM cells using GST switched from the covalent (RESET) to resonant (SET) bonding states by driving a current of 0.3 mA with a 3 V pulse for 100 ns (90 pJ). In stark contrast, the same operation was possible in identical PCRAM cells containing

the IPCM material by supplying a current of 0.15 mA with a 1.5 V pulse for 50 ns (11 pJ). It should be noted that it was possible to SET IPCM devices with shorter pulse durations and therefore the IPCM devices switching energy consumption should be considered a conservative estimate. Similarly, improvements in device efficiency were also possible for the RESET operation, with 0.73 mA at 3.5 V for 50 ns being required for the IPCM cells, compared with 1.25 mA at 6 V for 50 ns for GST-based cells. Further measurement details are given in the Supplementary Information.

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Author contributions

J.T. conceived and designed the entropy controlled interfacial phase-change memory structures. J.T., R.E.S. and T.Y. performed the experiments. R.E.S. wrote the paper. All authors analysed the results and contributed to the discussion presented in the manuscript.

Additional information

The authors declare no competing financial interests. Supplementary information accompanies this paper at www.nature.com/naturenanotechnology. Reprints and permission information is available online at <http://www.nature.com/reprints>. Correspondence and requests for materials should be addressed to R.E.S. and J.T.